Appln No. 10/747,931 Amdt date March 31, 2005 Preliminary Amendment

Amendments to the Specification:

Replace the paragraph beginning at page 9, line 26 with the following:

The signals from the converters [[32]] 34 and 36 pass to a complex multiplier 66 the construction of which will be described in detail subsequently. The outputs from the complex multiplier 66 are in turn introduced to a variable interpolator 68. Output connections are respectively made from the variable interpolator 68 to Nyquist filters 70 and 72 respectively corresponding to the filters 42 and 44 in Figure 1. The outputs from the filters 70 and 72 are connected to a forward error correction stage 74 corresponding to the stage 46 in Figure 1.